

EXPRESS MAIL NO. EV 327 134 640 US

DATE OF MAILING: 8/20/03

Our Case No. 10125-4117

Kims Ref. No.: OPP-EZ-2000-0117-US-C2

LGP's Ref. No.: P00-083US004D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Chae Gee Sung)
Serial No.: To Be Assigned)
Filing Date: Herewith)
For: Semiconductor Transistor Having)
Interface Layer Between)
Semiconductor And Insulating Layers)

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination of the above-identified application, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 5 of this paper.